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MULTIPLE SOFT FAULT DIAGNOSIS OF NONLINEAR DC CIRCUITS CONSIDERING COMPONENT TOLERANCES

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Abstract

This paper is devoted to multiple soft fault diagnosis of analog nonlinear circuits. A two-stage algorithm is offered enabling us to locate the faulty circuit components and evaluate their values, considering the component tolerances. At first a preliminary diagnostic procedure is performed, under the assumption that the non-faulty components have nominal values, leading to approximate and tentative results. Then, they are corrected, taking into account the fact that the non-faulty components can assume arbitrary values within their tolerance ranges. This stage of the algorithm is carried out using the linear programming method. As a result some ranges are obtained including possible values of the faulty components. The proposed approach is illustrated with two numerical examples.

Keywords: analog circuits, fault diagnosis, multiple faults, nonlinear circuits.

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1. Introduction

Fault diagnosis of analog circuits is an open problem for design validation and testing of electronic devices [1-30]. A fault can be catastrophic (hard) if it leads to some topological changes (open circuit or short circuit) or soft if a parameter drifted from its tolerance range. Fault diagnosis includes detecting faulty circuits, locating faulty components and evaluating their values. If most circuit simulations take place before any testing, the diagnostic method is classified as the simulation-before-test (SBT) approach, otherwise the method is classified as the simulation-after-test (SAT) approach.

Soft fault diagnosis is usually carried out using the SAT approach. Most of the works in this area are limited to a single fault, e.g. [6, 10, 12, 16, 21-23, 27]. Less papers have been devoted to multiple faults, e.g. [8, 11, 13, 17-18, 26]. To perform soft fault diagnosis a diagnostic test is arranged leading to diagnostic equations including circuit parameters as variables. For nonlinear circuits the diagnostic equation usually cannot be formulated in explicit analytical form. In addition, a difficult problem is fault masking due to scattering of the circuit parameters within their tolerance ranges.

This paper deals with multiple soft fault diagnosis of DC nonlinear circuits, takes into account component tolerances and offers a two-stage algorithm. It enables us to locate the faulty components and evaluate their parameters, considering the component tolerances. At first a preliminary diagnostic procedure developed in Section 2 is performed, under the assumption that the non-faulty components have nominal values. The obtained results are approximate and considered as tentative. Then they are corrected, taking into account the fact that the non-faulty components can assume arbitrary values within their tolerance ranges. This stage of the algorithm is performed using the linear programming method described in Section 3. The proposed approach is illustrated with two numerical examples placed in Section 4.

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To perform the first stage of the algorithm a diagnostic test is arranged. For this purpose we apply DC voltage sources to nodes accessible for excitation and measure voltages at accessible nodes. For different values of the voltage sources we obtain *n* values of the voltages u_1, \ldots, u_n and form a vector $\boldsymbol{u} = [u_1 \ldots u_n]^T$, where *n* is the number of circuit components considered as possibly faulty. Let $\boldsymbol{x} = [x_1 \ldots x_n]^T$ be a vector of the circuit parameters. Then the test equation has the form

$$f(\mathbf{x}) = \mathbf{u} \,, \tag{1}$$

where $f(\mathbf{x}) = [f_1(\mathbf{x}) \dots f_n(\mathbf{x})]^T$ is a nonlinear function. Generally, this function is not given in an explicit analytical form. However, the values of $f_i(\mathbf{x})$ $(i = 1, \dots, n)$ and their derivatives with respect to x_i $(j = 1, \dots, n)$ can be found numerically for given values of x_1, \dots, x_n .

2. Preliminary multiple fault diagnosis

Let $\mathbf{x}^{(0)}$ be a vector composed of nominal values of the circuit components. We expand the function $f(\mathbf{x})$ appeared in (1) into the Taylor series about $\mathbf{x}^{(0)}$ and neglect the higher order terms, similarly as in [18]

$$f(\mathbf{x}) = f(\mathbf{x}^{(0)}) + \left[\frac{\mathrm{d}f}{\mathrm{d}\mathbf{x}}(\mathbf{x}^{(0)})\right](\mathbf{x} - \mathbf{x}^{(0)}).$$
(2)

Since the function $f(\mathbf{x})$ is not given in explicit analytical form we cannot find $f(\mathbf{x}^{(0)})$ and $\left(\frac{\mathrm{d}f}{\mathrm{d}\mathbf{x}}\right)(\mathbf{x}^{(0)})$ directly. To overcome this drawback we set $\mathbf{x} = \mathbf{x}^{(0)}$ and carry out the DC and the sensitivity analyses of the circuit. Substituting (1) into (2) yields

$$A\mathbf{y} = \mathbf{b} \,, \tag{3}$$

where: $A = \frac{df}{dx}(x^{(0)})$ is an $n \times n$ matrix, $y = x - x^{(0)}$ and $b = u - f(x^{(0)})$ are *n*-vectors. The elements of y are deviations of the circuit components from the nominal values. To solve the equation (3) we first determine the rank of A applying the singular value decomposition (SVD) [31]. Let the rank of A be r (rank (A) = r), where $r \le n$. We choose an integer $p \le r$ such that p = 2 in the case of single fault diagnosis, p = 3 in the case of double fault diagnosis, and so on. We create all $n \times p$ submatrices of the matrix A having the rank equal to p using the procedure described in the Appendix. Let the number of these submatrices be M and $A^{(i)}$, where $i \in \{1, ..., M\}$ be an arbitrary matrix belonging to this set. We write the equation

$$\boldsymbol{A}^{(i)}\boldsymbol{y}^{(i)} = \boldsymbol{b} \,, \tag{4}$$

where $y^{(i)}$ is a vector consisting of p elements corresponding to the columns of the matrix A used to form $A^{(i)}$. To solve the equation (4) we apply the method of normal equation:

$$\left(\boldsymbol{A}^{(i)}\right)^{\mathrm{T}}\boldsymbol{A}^{(i)}\boldsymbol{y}^{(i)} = \left(\boldsymbol{A}^{(i)}\right)^{\mathrm{T}}\boldsymbol{b}$$
(5)

and denote the solution by $\mathbf{y}_1^{(i)}$. Adding the elements of $\mathbf{y}_1^{(i)}$ to the corresponding elements of $\mathbf{x}^{(0)}$ we obtain a vector, labeled $\mathbf{x}_1^{(i)}$, that contains p new values of the circuit components. The remaining elements of this vector are the same as in $\mathbf{x}^{(0)}$, i.e. they are equal to the nominal values. The obtained $\mathbf{x}_1^{(i)}$ is an approximate, specific solution of the test equation. To improve the solution the following iteration process, exploiting the idea of the Newton-Raphson method, is proposed. For $\mathbf{x}_1^{(i)}$ we find $f(\mathbf{x}_1^{(i)})$ and $\frac{df}{d\mathbf{x}}(\mathbf{x}_1^{(i)})$, similarly as we did with $\mathbf{x}^{(0)}$ and form up-dated matrix $A_1^{(i)}$ and vector $\mathbf{b}_1^{(i)} = \mathbf{u} - f(\mathbf{x}_1^{(i)})$. Then we solve the equation (4), with $A^{(i)}$ replaced by $A_1^{(i)}$ and \mathbf{b} replaced by $\mathbf{b}_1^{(i)}$, using the equation (5) adapted to this case. The solution is labelled $\mathbf{y}_2^{(i)}$, whereas the corresponding parameter vector, $\mathbf{x}_2^{(i)}$. The described process is continued, leading to the sequence $\mathbf{y}_1^{(i)}, \mathbf{y}_2^{(i)}, \dots, \mathbf{y}_l^{(i)}$, where it is assumed that *l*-th element (vector) $\mathbf{y}_l^{(i)}$ of this sequence satisfies a convergence criterion. Next we compute $\tilde{\mathbf{y}}^{(i)} = \sum_{j=1}^{l} \mathbf{y}_j^{(i)}$. Thus, any element of $\tilde{\mathbf{y}}^{(i)}$ is a total perturbation of the corresponding circuit component value.

Since the number of the submatrices $A^{(i)}$, having the rank p, is M we repeat the above described procedure for all of them. As a result a set $\{\tilde{\mathbf{y}}^{(i)}\}$ consisting of \hat{M} elements is created, where \hat{M} can be equal to M or smaller than M, if some of the sequences $\{\mathbf{y}_{1}^{(i)}, \mathbf{y}_{2}^{(i)}, \ldots\}$ are not convergent in a preset number of iterations L_{\max} . The elements of different vectors $\tilde{\mathbf{y}}^{(i)}$ of the set $\{\tilde{\mathbf{y}}^{(i)}\}$ correspond to different combinations of p circuit components. Thus, the set $\{\tilde{\mathbf{y}}^{(i)}\}$ consists of \hat{M} elements (vectors) $\tilde{\mathbf{y}}^{(i)} = [\tilde{y}_{i_1} \ldots \tilde{y}_{i_p}]^T$, where i_1, \ldots, i_p belong to the set $\{1, 2, \ldots, n\}$.

Procedure for selection of the faulty components

The procedure described underneath enables us to locate up to p-1 faulty circuit components and evaluate their approximate values. Let us pick up arbitrarily p-1 numbers from the set $\{1, 2, ..., n\}$ and select all the *p*-dimension vectors of the set $\{\tilde{y}^{(i)}\}$ containing the elements specified by the chosen p-1 numbers. Each of the selected *p*-dimension vectors $\tilde{y}^{(i)}$ contains p-1 elements corresponding to the same p-1 circuit components and one additional element. Let the number of these vectors be η . We choose all the vectors containing this additional element equal to zero (close to zero) and check if their number $\hat{\eta} \ge c\eta$, where *c* has been set 0.5 and $\eta \ge 4$ on the basis of numerical experiments. If it holds and values of each of the p-1 elements in all the $\hat{\eta}$ vectors are sufficiently close, the p-1circuit components are classified as possibly faulty. Their values are obtained by adding each of the p-1 elements to the corresponding their values is carried out for all combinations of p-1 elements out of *n* elements, i.e. C_n^{p-1} combinations.

The developed method enables us to find a set of faulty circuit components if their number does not exceed p-1. Sometimes the method gives additionally one or more virtual sets that

also satisfy the diagnostic test, being the result of existence of some ambiguity groups. Thus, generally several vectors x of the circuit components can be found.

3. Correction of the faults considering the component tolerances

The preliminary method developed in Section 2 enables us to locate faulty components and evaluate their values, under the assumption that the remaining components have nominal values. In reality, however, the components are not nominal but scattered within their tolerance frames. To take into account the influence of the disturbance we correct the obtained results [30].

Let us consider a vector \mathbf{x} containing the evaluated values of faulty components and the nominal values of the remaining components, obtained as described in Section 2 and label it \mathbf{x}^* . Without any loss of generality we can assume that the first p-1 elements of the vector \mathbf{x}^* correspond to the circuit components that have been diagnosed as faulty. Consequently, the vector \mathbf{x}^* can be decomposed into two subvectors: $\mathbf{r}^* = [r_1^* \cdots r_{p-1}^*]^T$ corresponding to the faulty components and $\mathbf{z}^* = [z_p^* \cdots z_n^*]^T$ corresponding to the nominal components,

$$\boldsymbol{x}^* = \begin{bmatrix} \boldsymbol{r}^* \\ \boldsymbol{z}^* \end{bmatrix}. \tag{6}$$

In this section we assume that the non-faulty components are allowed to be arbitrarily dissipated within their tolerance frames and find deviations of the values of the faulty components from r_1^*, \dots, r_{p-1}^* , taking into account the results of the test. Thus, we obtain some ranges of the values of the faulty components near the values r_1^*, \dots, r_{p-1}^* , due to scattering of the values of the non-faulty components within their tolerance frames.

Let the non-faulty components have their values z_i belonging to the tolerance ranges

$$z_j = z_j^* + \Delta z_j, \ j = p, ..., n,$$
 (7)

where

$$-\varepsilon_j \le \Delta z_j \le \varepsilon_j. \tag{8}$$

Thus, instead of the vector z^* we consider the vector $z = [z_p \cdots z_n]^T$. The perturbations of the non-faulty components, within their tolerance frames, cause some perturbations of the obtained values of the faulty components, leading to the vector

$$\boldsymbol{r} = \boldsymbol{r}^* + \Delta \boldsymbol{r} \,, \tag{9}$$

where: $\Delta \boldsymbol{r} = [\Delta r_1 \cdots \Delta r_{p-1}]^{\mathrm{T}}$,

$$r_j = r_j^* + \Delta r_j, \ j = 1, ..., p - 1.$$
 (10)

Thus, we need the lower and upper bounds on Δr_j , (j = 1, ..., p-1), labelled Δr_j^- and Δr_j^+ respectively, so that

$$\Delta r_i^- \le \Delta r_i \le \Delta r_i^+ \,. \tag{11}$$

For this purpose we use the linear programming approach.

Let us define some range $\left[-\delta_{j}, \delta_{j}\right]$ in which Δr_{j} is sought

$$-\delta_j \le \Delta r_j \le \delta_j, \ j = 1, \dots, p-1$$
(12)

and introduce new variables $\Delta \tilde{r}_i$

$$\Delta \tilde{r}_j = \Delta r_j + \delta_j, \qquad (13)$$

which, according to (12) satisfy the inequalities

$$0 \le \Delta \tilde{r}_j \le 2\delta_j, \ j = 1, \dots, p-1.$$

$$(14)$$

Similarly we introduce new variables $\Delta \tilde{z}_i$

$$\Delta \tilde{z}_j = \Delta z_j + \varepsilon_j, \tag{15}$$

which, according to (8), satisfy the inequalities

$$0 \le \Delta \tilde{z}_j \le 2\varepsilon_j, \ j = p, \dots, n.$$
(16)

On the basis of the test equation (1) we write

$$\hat{f}(\boldsymbol{x}^*) + \frac{\partial \hat{f}}{\partial \boldsymbol{r}}(\boldsymbol{x}^*)(\boldsymbol{r} - \boldsymbol{r}^*) + \frac{\partial \hat{f}}{\partial z}(\boldsymbol{x}^*)(\boldsymbol{z} - \boldsymbol{z}^*) = \hat{\boldsymbol{u}}, \qquad (17)$$

where: $\mathbf{x}^* = \left[(\mathbf{r}^*)^{\mathrm{T}} (\mathbf{z}^*)^{\mathrm{T}} \right]^{\mathrm{T}}$, $\hat{\mathbf{f}}$ and $\hat{\mathbf{u}}$ are \hat{n} -vectors selected from \mathbf{f} and \mathbf{u} , respectively, where $\hat{n} < n$. To choose \hat{n} and select \hat{n} appropriate equations from the system of n test equations we use the following procedure. First we determine the rank of the matrix $\frac{\partial f}{\partial \mathbf{x}}(\mathbf{x}^*)$ and label it \overline{n} . If $\overline{n} = n$ then $\hat{n} = n - 1$, otherwise $\hat{n} = \overline{n}$. Then, we consider all combinations of \hat{n} equations from among n ones and select this combination which fulfils two requirements: $\operatorname{rank}\left(\frac{\partial \hat{f}}{\partial \mathbf{x}}(\mathbf{x}^*)\right) = \hat{n}$ and $\|\hat{\mathbf{u}} - \hat{f}(\mathbf{x}^*)\|_2$ is minimal.

The equation (17) can be written in the form

$$\sum_{j=1}^{p-1} s_{ij} \Delta r_j + \sum_{j=p}^n s_{ij} \Delta z_j = d_i, \ i = 1, \dots, \hat{n},$$
(18)

where: $d_i = \hat{u}_i - \hat{f}_i(\mathbf{x}^*), \ i = 1, ..., \hat{n}$, and

$$s_{ij} = \begin{cases} \frac{\partial \hat{f}_i}{\partial r_j} (\mathbf{x}^*) & j = 1, \dots, p-1, \\ \frac{\partial \hat{f}_i}{\partial z_j} (\mathbf{x}^*) & j = p, \dots, n. \end{cases}$$
(19)

Substituting $\Delta r_j = \Delta \tilde{r}_j - \delta_j$ (see(13)) and $\Delta z_j = \Delta \tilde{z}_j - \varepsilon_j$ (see (15)) into (18) yields

$$\sum_{j=1}^{p-1} s_{ij} \Delta \tilde{r}_j + \sum_{j=p}^n s_{ij} \Delta \tilde{z}_j = \tilde{d}_i \quad , \ i = 1, \dots, \hat{n} ,$$

$$(20)$$

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where:

$$\widetilde{d}_i = d_i + \sum_{j=1}^{p-1} s_{ij} \delta_j + \sum_{j=p}^n s_{ij} \varepsilon_j .$$
(21)

We select all the equations of the set (20) having $\tilde{d}_i < 0$ and multiply them by (-1). As a result we obtain a system of equations

$$\sum_{j=1}^{p-1} \hat{s}_{ij} \Delta \tilde{r}_j + \sum_{j=p}^n \hat{s}_{ij} \Delta \tilde{z}_j = \hat{d}_i , \ i = 1, ..., \hat{n} ,$$
(22)

where $\hat{d}_i \ge 0$ $(i = 1, ..., \hat{n})$. Let us introduce the slack variables $\tilde{w}_j \ge 0$ (j = 1, ..., n) so that

$$\Delta \tilde{r}_j + \tilde{w}_j = 2\delta_j, \ j = 1, \dots, p-1,$$
(23)

$$\Delta \tilde{z}_j + \tilde{w}_j = 2\varepsilon_j, \ j = p, \dots, n \,. \tag{24}$$

For each $j \in \{1, ..., p-1\}$ we formulate the linear programming problem:

maximize $\Delta \tilde{r}_i$

$$\sum_{j=1}^{p-1} \hat{s}_{ij} \Delta \widetilde{r}_j + \sum_{j=p}^n \hat{s}_{ij} \Delta \widetilde{z}_j = \hat{d}_i , i = 1, ..., \hat{n} ,$$

$$\Delta \widetilde{r}_j + \widetilde{w}_j = 2\delta_j , j = 1, ..., p-1 ,$$

$$\Delta \widetilde{z}_j + \widetilde{w}_j = 2\varepsilon_j , j = p, ..., n ,$$

$$\Delta \widetilde{r}_j \ge 0, \Delta \widetilde{z}_j \ge 0, \widetilde{w}_j \ge 0$$
(25)

subject to

and solve it using the simplex method [32-33]. As a result we find: $(\Delta \tilde{r}_1)_{max}, ..., (\Delta \tilde{r}_{p-1})_{max}$. Similarly we find $(\Delta \tilde{r}_1)_{min}, ..., (\Delta \tilde{r}_{p-1})_{min}$ minimizing $\Delta \tilde{r}_j$ for j = 1, 2, ..., p-1. Using (13), yields

$$\Delta r_j^+ = \left(\Delta r_j\right)_{max} = \left(\Delta \tilde{r}_j\right)_{max} - \delta_j , \quad j = 1, \dots, p-1,$$
(26)

$$\Delta r_j^- = \left(\Delta r_j\right)_{min} = \left(\Delta \tilde{r}_j\right)_{min} - \delta_j, \quad j = 1, \dots, p-1.$$
⁽²⁷⁾

Thus, we find the region including the values of the faulty components:

 $(\mathbf{r}^* + \Delta \mathbf{r}^-, \mathbf{r}^* + \Delta \mathbf{r}^+)$, where $\Delta \mathbf{r}^- = [\Delta r_1^- \cdots \Delta r_{p-1}^-]^T$, $\Delta \mathbf{r}^+ = [\Delta r_1^+ \cdots \Delta r_{p-1}^+]^T$, valid for actual values of the non-faulty components belonging to their tolerance ranges.

4. Numerical examples

The proposed method was implemented in MATLAB and tested using several transistor circuits. Two of them are presented below. The calculations were executed on a PC Pentium Core 2 Duo E 6400.

Example 1

Consider the circuit shown in Fig. 1 [30], where nominal values of the circuit components are indicated, and their tolerance is 5%. All seven resistors of the circuit are considered as

possibly faulty. We assume that the nodes A and B are accessible for measurement. The transistors are characterized by the Ebers-Moll model with the following parameters: $\alpha_F = 0.9975$, $\alpha_R = 0.8$, $I_{ES} = 10.22$ fA, $I_{CS} = 12.75$ fA, $V_T = 25.86$ mV. To perform a diagnostic test we measure voltages at the test nodes A and B at the three combinations of the voltage sources: $v_{in}^{(1)} = 15$ V, $v_{in}^{(2)} = 15$ V; $v_{in}^{(1)} = 5$ V, $v_{in}^{(2)} = 15$ V; $v_{in}^{(1)} = 15$ V, $v_{in}^{(2)} = 0$ V and at the test node B for $v_{in}^{(1)} = 6$ V, $v_{in}^{(2)} = 6$ V, assuming an accuracy of 10 μ V. Underneath, four cases are described assuming $\varepsilon_j = 0.05z_j^*$ and $\delta_j = 0.20r_j^*$ in the correction procedure.



Fig. 1. A transistor circuit for Example 1.

Case 1

Components $R_1 = 175.5 \text{ k}\Omega$, $R_2 = 14.30 \text{ k}\Omega$, $R_6 = 175.5 \Omega$ are faulty (-35%, -35%, -35%) and the others are within their tolerance ranges: $R_3 = 44.2 \text{ k}\Omega$, $R_4 = 6.95 \text{ k}\Omega$, $R_5 = 2.78 \text{ k}\Omega$, $R_7 = 277.0 \Omega$. The preliminary method leads to the correct set of the faulty components $\{R_1, R_2, R_6\}$. The correction procedure, described in Section 3, gives the following results: $R_1 \in (162.2 - 177.4) \text{ k}\Omega$, $R_2 \in (13.26 - 14.71) \text{ k}\Omega$, $R_6 \in (162.8 - 180.2) \Omega$, including the actual values of the components.

Case 2

Components $R_3 = 21.5 \text{ k}\Omega$, $R_4 = 3.4 \text{ k}\Omega$, $R_6 = 135 \Omega$ are faulty (-50%, -50%, -50%)and the others are within their tolerance ranges: $R_1 = 265.0 \text{ k}\Omega$, $R_2 = 21.0 \text{ k}\Omega$, $R_5 = 2.78 \text{ k}\Omega$, $R_7 = 277.0 \Omega$. The preliminary method leads to the correct set of the faulty components $\{R_3, R_4, R_6\}$, whereas the correction procedure gives the following results: $R_3 \in (20.80 - 22.18) \text{ k}\Omega$, $R_4 \in (3.40 - 3.42) \text{ k}\Omega$, $R_6 \in (133.67 - 137.82) \Omega$, including the actual values of the components.

Case 3

Components $R_3 = 58.05 \text{ k}\Omega$, $R_4 = 9.18 \text{ k}\Omega$, $R_7 = 364.5 \Omega$ are faulty (35%, 35%, 35%)and the others are within their tolerance ranges: $R_1 = 265.0 \text{ k}\Omega$, $R_2 = 21.0 \text{ k}\Omega$, $R_5 = 2.78 \text{ k}\Omega$, $R_6 = 278.0 \Omega$. The preliminary method leads to three sets of components: $\{R_1, R_4, R_7\}$, $\{R_2, R_3, R_7\}$, and $\{R_3, R_4, R_7\}$. The last is the real one, whereas the others are virtual. The correction procedure eliminates the first set and gives the following results: $R_2 \in (14.66 - 16.24) \,\mathrm{k\Omega}$, $R_3 \in (57.82 - 62.56) \,\mathrm{k\Omega}$, $R_7 \in (363.8 - 402.7) \,\mathrm{\Omega}$ and $R_3 \in (56.12 - 59.10) \,\mathrm{k\Omega}$, $R_4 \in (9.14 - 10.08) \,\mathrm{k\Omega}$, $R_7 \in (356.7 - 373.5) \,\mathrm{\Omega}$. Thus, the method provides the ranges of the actual values of the faulty components and another ranges of values of the virtual set of components.

Case 4

Components $R_1 = 540 \text{ k}\Omega$, $R_2 = 44 \text{ k}\Omega$, $R_5 = 5.4 \text{ k}\Omega$ are faulty (100%, 100%, 100%) and the others are within their tolerance ranges: $R_3 = 44.2 \text{ k}\Omega$, $R_4 = 6.95 \text{ k}\Omega$, $R_6 = 278.0 \Omega$, $R_7 = 277.0 \Omega$. The preliminary method leads to the correct set of the faulty components $\{R_1, R_2, R_5\}$. The correction procedure gives the following results: $R_1 \in (498.68 - 551.74) \text{ k}\Omega$, $R_2 \in (40.81 - 45.22) \text{ k}\Omega$, $R_5 \in (5.00 - 5.50) \text{ k}\Omega$, including the actual values of the components.

Some statistical results

For statistical analysis 112 sets of faulty components $(\pm 35\%)$ were considered, including all combinations of double (42) and triple (70) faults. The method found the faulty set in 83.0%, in 59.8% uniquely. In each case the provided ranges of values of the faulty components effectively framed the actual values. In 12.5% the preliminary method failed. In 4.5% of the cases, values evaluated by the preliminary method were far away from the actual values and they were discarded during the first phase of the simplex method. The CPU time in each case was less than 5s.

Example 2



Fig. 2. A transistor circuit for Example 2.

Let us consider the transistor circuit shown in Fig. 2, where nominal values of the circuit components are indicated. The tolerance of the components is 5%. All six resistors of the circuit are considered as possibly faulty. We assume that the nodes A, B, and C are accessible for measurement. The transistors are characterized by the Ebers-Moll model with the following parameters: $\alpha_F = 0.9975$, $\alpha_R = 0.8$, $I_{ES} = 10.22$ fA, $I_{CS} = 12.75$ fA, $V_T = 25.86$ mV. To perform a diagnostic test we measure voltages at the test nodes for $v_{in}^{(1)} = 12$ V and $v_{in}^{(2)} = 5$ V, assuming the accuracy to be 1μ V. We wish to diagnose double

(p=3) and triple (p=4) faults. Underneath, three cases are described assuming in the correction procedure $\varepsilon_i = 0.05 z_i^*$ and $\delta_i = 0.20 r_i^*$.

Case 1

Components $R_1 = 4.94 \text{ k}\Omega$, $R_4 = 253.5 \Omega$ are faulty (-35%, -35%) and the others are within their tolerance ranges: $R_2 = 4.87 \text{ k}\Omega$, $R_3 = 385.0 \Omega$, $R_5 = 9.89 \text{ k}\Omega$, $R_6 = 102 \text{ k}\Omega$. The preliminary method, described in Section 2, leads to two sets of faulty components $\{R_1, R_4\}$ and $\{R_2, R_3\}$, where the first set is true and the other is virtual. During the correction procedure, described in Section 3, the second set is eliminated. The correction procedure gives the following ranges of values of the faulty components: $R_1 \in (4.940 - 4.941) \text{ k}\Omega$, $R_4 \in (253.5 - 253.6)\Omega$, that very well frame the actual values.

Case 2

Components $R_1 = 12.0 \text{ k}\Omega$, $R_2 = 9.00 \text{ k}\Omega$, and $R_5 = 13.5 \text{ k}\Omega$ are faulty (58%, 80%, 35%)and the others are within their tolerance ranges: $R_3 = 385.0 \Omega$, $R_4 = 392.0 \Omega$, $R_6 = 102 \text{ k}\Omega$. The preliminary method gives the correct set of faulty components $\{R_1, R_2, R_5\}$. The correction procedure leads to the following ranges of values of the faulty components: $R_1 \in (11.999 - 12.002) \text{ k}\Omega$, $R_2 \in (8.984 - 9.018) \text{ k}\Omega$, $R_5 \in (10.790 - 16.184) \text{ k}\Omega$, including the actual values of the components.

Case 3

Components $R_4 = 253.5 \Omega$, $R_5 = 6.5 k\Omega$ are faulty (-35%, -35%) and the others are within their tolerance ranges: $R_1 = 7.4 k\Omega$, $R_2 = 4.87 k\Omega$, $R_3 = 385.0 \Omega$, $R_6 = 102 k\Omega$. The preliminary method, described in Section 2, leads to four sets of faulty components $\{R_1, R_4\}$, $\{R_2, R_4\}$, $\{R_3, R_4\}$, and $\{R_4, R_5\}$, where the last one is true and the others are virtual. During the correction procedure, the second set is eliminated leading to the following results: $R_1 \in (7.3970 - 7.3973) k\Omega$, $R_4 \in (253.39 - 253.40) \Omega$; $R_3 \in (384.45 - 384.47) \Omega$, $R_4 \in (253.30 - 253.31) \Omega$; $R_4 \in (253.35 - 253.44) \Omega$, $R_5 \in (4.740 - 7.110) k\Omega$, where the last set is true and the others are virtual.

Some statistical results

In the discussed circuit 40 sets of faulty circuit components were considered, including 25 double and 15 triple faults. The method found the faulty set in 87.5%, in 72.5% uniquely. In each case the provided ranges of values of the faulty components effectively framed the actual values. In 12.5% the preliminary method failed. The CPU time in each case was between 2 and 10 seconds.

5. Conclusions

The two-stage algorithm developed in this paper enables us to perform multiple soft fault diagnosis of DC nonlinear circuits, even if the deviations of the faulty components from their

nominal values are large. The diagnosis includes location of faulty components and evaluation of their values, considering the component tolerances. The results are given in the form of some ranges where the values of the faulty components are contained. The numerical experiments carried out show that the evaluated ranges effectively frame the actual values of the faulty components. The method can be extended to dynamic circuits. The proposed approach is especially useful at the pre-production stage, where corrections of the technological process are possible and the time consumed by the diagnostic procedure is not crucial.

Appendix

Creating the submatrices $A^{(i)}$ having the rank p

To determine the rank of the $n \times n$ matrix A the singular value decomposition (SVD) [31] is applied

$$A = U\Sigma V^{\mathrm{T}}, \qquad (A.1)$$

where: U and V are orthogonal $n \times n$ matrices, Σ is an $n \times n$ matrix of the form

$$\boldsymbol{\Sigma} = \begin{bmatrix} \boldsymbol{\sigma}_1 & & \\ & \ddots & & \mathbf{0} \\ & & & \boldsymbol{\sigma}_r \\ & & & \boldsymbol{\sigma}_r \\ & & & \mathbf{0} \end{bmatrix}, \qquad (A.2)$$

where: $\sigma_1 \ge \sigma_2 \ge \cdots \ge \sigma_r > 0$ are called singular values of A and r is the rank of A $(r = \operatorname{rank}(A))$. Note that

$$\boldsymbol{\Sigma}\boldsymbol{V}^{\mathrm{T}} = \begin{bmatrix} \boldsymbol{L} \\ \boldsymbol{0} \end{bmatrix}, \tag{A.3}$$

where: L is an $r \times n$ matrix and 0 is $(n-r) \times n$ zero matrix. Taking into account the equations (A.1) and (A.3) we obtain

$$A = KL, \qquad (A.4)$$

where: **K** is an $n \times r$ matrix composed of the first r columns of **U**. Since rank (A) = r, the matrix **K** has also the rank r. We consider all $r \times p$ submatrices of the $r \times n$ matrix **L** and select these of them which have the rank equal to p using SVD. Let us denote the submatrices by $L^{(i)}$, i = 1, ..., M. For an arbitrary $r \times p$ submatrix $L^{(i)}$ the corresponding $n \times p$ submatrix $A^{(i)}$ of A, $A^{(i)} = KL^{(i)}$ has the rank equal to p (see Lemma 1).

Thus, to find all the $n \times p$ submatrices $A^{(i)}$ of A having the rank p we operate with smaller submatrices $L^{(i)}$. For each $L^{(i)}$ we check which columns of L have been selected to form $L^{(i)}$ and use the same columns of A to create $A^{(i)}$. In this way the procedure for creating the $n \times p$ submatrices $A^{(i)}$ having the rank p is simplified.

Lemma 1

Let us consider an $n \times r$ matrix K $(n \ge r)$ and an $r \times p$ matrix S, where rank (K) = r, rank (S) = p. Then the matrix KS has the rank equal to p.

Proof

Let $S = [s_1 \dots s_p]$, $K = [k_1 \dots k_r]$, where s_i is *i*-th column of the matrix S and k_i is *i*-th column of the matrix K. Then $KS = [Ks_1 \dots Ks_p]$ holds. Suppose that there exist such numbers c_1, \dots, c_p , not all equal to zero, that $c_1(Ks_1) + \dots + c_p(Ks_p) = K(c_1s_1 + \dots + c_ps_p) = 0$. Since S has rank p, its columns are linearly independent, hence, $c_1s_1 + \dots + c_ps_p \neq 0$. Thus, $w = [w_1 \dots w_r]^T = c_1s_1 + \dots + c_ps_p \neq 0$ and $w_1k_1 + \dots + w_rk_r = 0$, where not all numbers w_1, \dots, w_r are equal to zero. This is a contradiction because K has, by the assumption, rank r.

References

- [1] Bandler, J.W., Salama, A.E. (1985). Fault diagnosis of analog circuits. *Proc. IEEE*, 73, 1279-1325.
- [2] Zielonko, R., Królikowski, A. (1988). *Measurement-diagnostic methods for analog electronic circuits*. WNT, Warszawa. (in Polish)
- [3] Ozawa, T. (1988). Analog method for computer aided circuit analysis and diagnosis. M. Dekker, New York.
- [4] Rutkowski, J. (2003). Diagnostic dictionary methods of analog electronic circuits. WKŁ, Warszawa. (in Polish)
- [5] Kabisatpathy, P., Barua, A., Sinha, S. (2005). Fault diagnosis of analog integrated circuits. Springer, Dordrecht.
- [6] Rutkowski, J. (1993). A DC approach for analog fault dictionary determination. Proc. Europ. Con. Cir. Theor. Des., ECCTD'93, 877-880.
- [7] Materka, A., Strzelecki, M. (1996). Parametric testing of mixed-signal circuits by ANN processing of transient responses. *Journal of Electronic Testing*, 9, 187-202.
- [8] Fedi, G., Giomi, R., Luchetta, A., Manetti, S. Piccirilli, M. C. (1998). On the application of symbolic techniques to the multiple fault location in low testability analog circuit. *IEEE Trans. Cir. Syst. - II*, 45, 1383-1388.
- [9] Tadeusiewicz, M., Korzybski, M. (2000). A method for fault diagnosis in linear electronic circuits. Int. J. Cir. Theor. Appl., 28, 245-262.
- [10] Catelani, M., Fort, A. (2002). Soft fault detection and isolation in analog circuits: some results and a comparison between a fuzzy approach and radial basis function networks. *IEEE Trans. Instrum. Measur.*, 51, 196-202.
- [11] Liu, D., Starzyk, J.A. (2002). A generalized fault diagnosis in dynamic analog circuits. Int. J. Cir. Theor. Appl., 30, 487-510.
- [12] Robotycki, A., Zielonko, R. (2002). Fault diagnosis of analog piecewise linear circuits based on homotopy. *IEEE Trans. Instrum. Measur.*, 51, 876-881.
- [13] Tadeusiewicz, M., Hałgas, S., Korzybski, M. (2002). An algorithm for soft-fault diagnosis of linear and nonlinear circuits. *IEEE Trans. Cir. Syst. - I*, 49, 1648-1653.
- [14] Toczek, W. (2004). Analog fault signature based on sigma-delta modulation and oscillation-test methodology. *Metrology and Measurement Systems*, XI, 363-375.
- [15] Starzyk, J., Liu, D., Liu, Z., Nelson, D., Rutkowski, J. (2004). Entropy-based optimum test points selection for analog fault dictionary techniques. *IEEE Trans. Instrum. Measur.*, 53, 754-761.
- [16] Toczek, W., Kowalewski, M. (2005). A neural network based system for soft fault diagnosis in electronic circuits. *Metrology and Measurement Systems*, 12(4), 463-374.
- [17] Tadeusiewicz, M., Hałgas, S. (2006). An algorithm for multiple fault diagnosis in analogue circuits. Int. J. Cir. Theor. Appl., 34, 607-615.

- [18] Tadeusiewicz, M., Hałgas, S., Sidyk, P. (2007). A method of soft fault diagnosis in transistor circuits. Electronics – Constructions, Technologies, Applications, 11, 31-33. (in Polish)
- [19] Aminian, M., Aminian, F. (2007). A modular fault-diagnostic system for analog electronic circuits using neural networks with wavelet transform as a preprocessor. *IEEE Trans. Instrum. Measur.*, 56, 1546-1554.
- [20] Golonek, T., Rutkowski, J. (2007). Genetic-algorithm-based method for optimal analog test points selections. *IEEE Trans. Cir. Syst. - II*, 54, 117-131.
- [21] Kuczyński, A., Ossowski, M. (2009). Analog circuits diagnosis using discrete wavelet transform of supply current. *Metrology and Measurement Systems*, 16(1), 77-84.
- [22] Longfu, Zhou, Yibing, Shi, Jingyuan, Tang, Yanjun, Li. (2009). Soft fault diagnosis in analog circuit based on fuzzy and direction vector. *Metrology and Measurement Systems*, 16(1), 61-75.
- [23] Grzechca, D., Rutkowski, J. (2009). Fault diagnosis in analog electronic circuits the SVM approach. *Metrology and Measurement Systems*, 16(4), 583-598.
- [24] Tadeusiewicz, M., Hałgas, S. (2010). A method for fast simulation of multiple catastrophic faults in analogue circuits. *Int. J. Cir. Theor. Appl.*, 38, 275-290.
- [25] Załęski, D., Bartosiński, B., Zielonko, R. (2010). Application of complementary signals in Built-In Self Testers for mixed-signal embedded electronic systems. *IEEE Trans. Instrum. Measur.*, 59, 345-352.
- [26] Wei, Zhang, Longfu, Zhou, Yibing, Shi, Chengti, Huang, Yanjun, Li. (2010). Soft-fault diagnosis of analog circuit with tolerance using FNLP. *Metrology and Measurement Systems*, 17(3), 349-362.
- [27] Sałat, R., Osowski, S. (2011). Support Vector Machine for soft fault location in electrical circuits. Journal of Intelligent and Fuzzy Systems, 22, 21-31.
- [28] Tadeusiewicz, M., Hałgas, S., Korzybski, M. (2011). Multiple catastrophic fault diagnosis of analog circuits considering the component tolerances. *Int. J. Cir. Theor. Appl.*, published on line: 29 MAR 2011 | DOI: 10.1002/cta.770
- [29] Pułka, A. (2011). Two heuristic algorithms for test point selection in analog circuit diagnoses. *Metrology and Measurement Systems*, 18(1), 115-128.
- [30] Tadeusiewicz, M., Hałgas, S. (2011). Fault diagnosis of nonlinear circuits considering component tolerances. *Proceedings of X National Conference of Electronics*, KKE'11, 890-895. CD-ROM. (in Polish)
- [31] Golub, G.H., Van Loan, C.S. (1996). Matrix Computation. The Johns Hopkins University Press, London.
- [32] Simonnard, M. (1962). Programmation lineaire. Dunod: Paris.
- [33] Sierksma, G. (1996). Linear and integer programming: Theory and practice. Marcel Dekker, New York.